

EL 169866407

3 / IDS
C. Willis
11-22-99

1 Inventor: Eugene P. Marsh

2 Title: Circuitry Comprising Roughened Platinum Layers, Platinum-
3 Containing Materials, Capacitors Comprising Roughened Platinum
4 Layers, Methods of Forming Roughened Layers of Platinum, and
5 Methods of Forming Capacitors

6 Assignee: Micron Technology, Inc.

10559 U.S. PTO

09/421625



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8 INFORMATION DISCLOSURE STATEMENT

9 PURSUANT TO 37 C.F.R. §§1.56, 1.97 AND 1.98

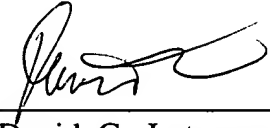
10 In compliance with 37 C.F.R. §§1.56, 1.97 and 1.98, your attention is
11 directed to the United States patents and other references listed on the
12 attached Form PTO-1449.

13 All listed references were cited by, or submitted to, the Office in the
14 parent, co-pending application of the above-identified application. The above-
15 identified application is a continuation of co-pending Application Serial
16 No. 09/281,735, filed March 30, 1999. Such prior disclosure is sufficient for
17 the above-identified application as far as copies of the references are
18 concerned.

1 Citation of these references is respectfully requested.

2 Respectfully submitted,

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4 Dated: 10/19/97

5 Attorney: 

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